

# AVB Base Module - Datasheet

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## Features

- Ethernet AVB in/out (1GBit/100MBit depending on processor resource usage)
- PoE powered
- Flexible audio interface (Supported per software option in combination with IO cards)
  - I2S in/out
  - SPDIF/AES3 in/out
  - ADAT in/out
- Programming/Debug interface
- Power supply for optional IO cards
- Optional GPIOs
- Optional UART control
- Optional USB interface

# Pinout

## Debug

Connector-Type: TAG-Connect TC2050NL

Pin	Signal name	Signal type
1	XL_DN1	X-Channel
2	XL_DN0	X-Channel
3	XL_UP0	X-Channel
4	XL_UP1	X-Channel
5	GND	GND
6	XSYS_RST_N	Reset
7	TMS	JTag
8	TCK	JTag
9	TDO	JTag
10	TDI	JTag

## USB

Connector type: FFC 0.5mm 6 pin

Pin	Signal name	Signal type
1	GND	Ground
2	USB_VBUS	USB
3	USB_ID	USB
4	USB_DP_P	USB
5	USB_DM_N	USB
6	GND	Ground

# System

Connector type: FFC 0.5mm 30 pin

Pin	Signal name	Signal type
1	5V0	Power-in/out
2	5V0	Power-in/out
3	3V3	Power-out
4	3V3	Power-out
5	GPIO_OUT	GPIO
6	ADC_RST_N_OUT	Reset (out)
7	N.C.	Not connected
8	GND	Ground
9	SDIOD_IN	I2S, Data in/out ( <b>default i2s in</b> )
10	GND	Ground
11	SDIOC_IN	I2S, Data in/out ( <b>default optical in</b> )
12	GND	Ground
13	SDIOB_OUT	I2S, Data in/out ( <b>default i2s out</b> )
14	GND	Ground
15	SDIOA_OUT	I2S, Data in/out ( <b>default optical out</b> )
16	GND	Ground
17	SCLK_OUT	I2S, Bit clock out
18	GND	Ground
19	LRCK_OUT	I2S, LR clock out
20	GND	Ground
21	MCLK_OUT	Master clock out
22	GND	Ground
23	SDA	I2C, data
24	SCL	I2C, clock
25	GND	Ground
26	GND	Ground

27	SDIOE_OUT	I2S, Data in/out (default SPDIF-TX)
28	GND	Ground
29	SDIOF_IN	I2S, Data in/out
30	GND	Ground

# Dimensions

